

Fig 7.1 Fault Finding Flowchart - Part 1

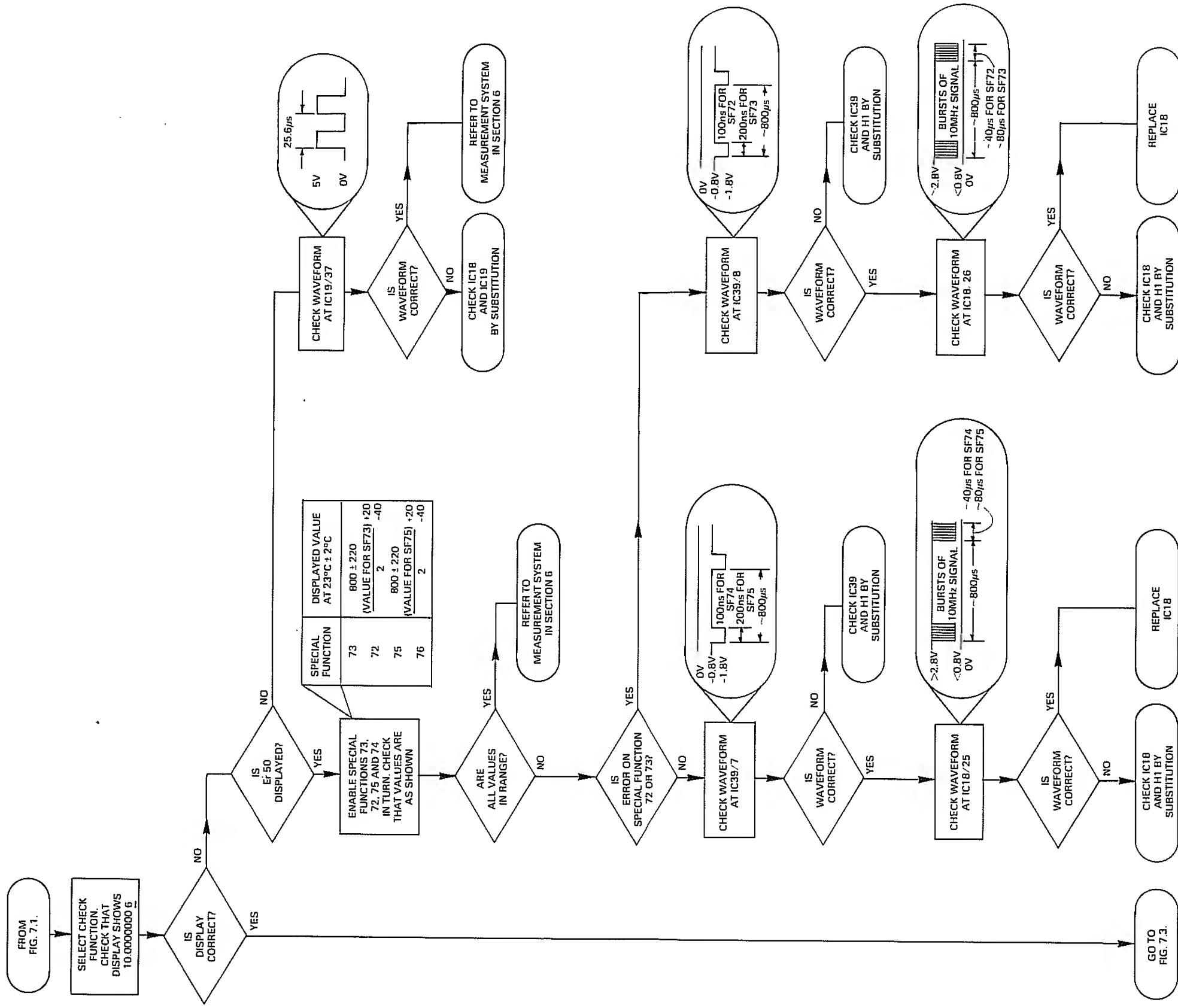


Fig 7.2 Fault Finding Flowchart - Part 2

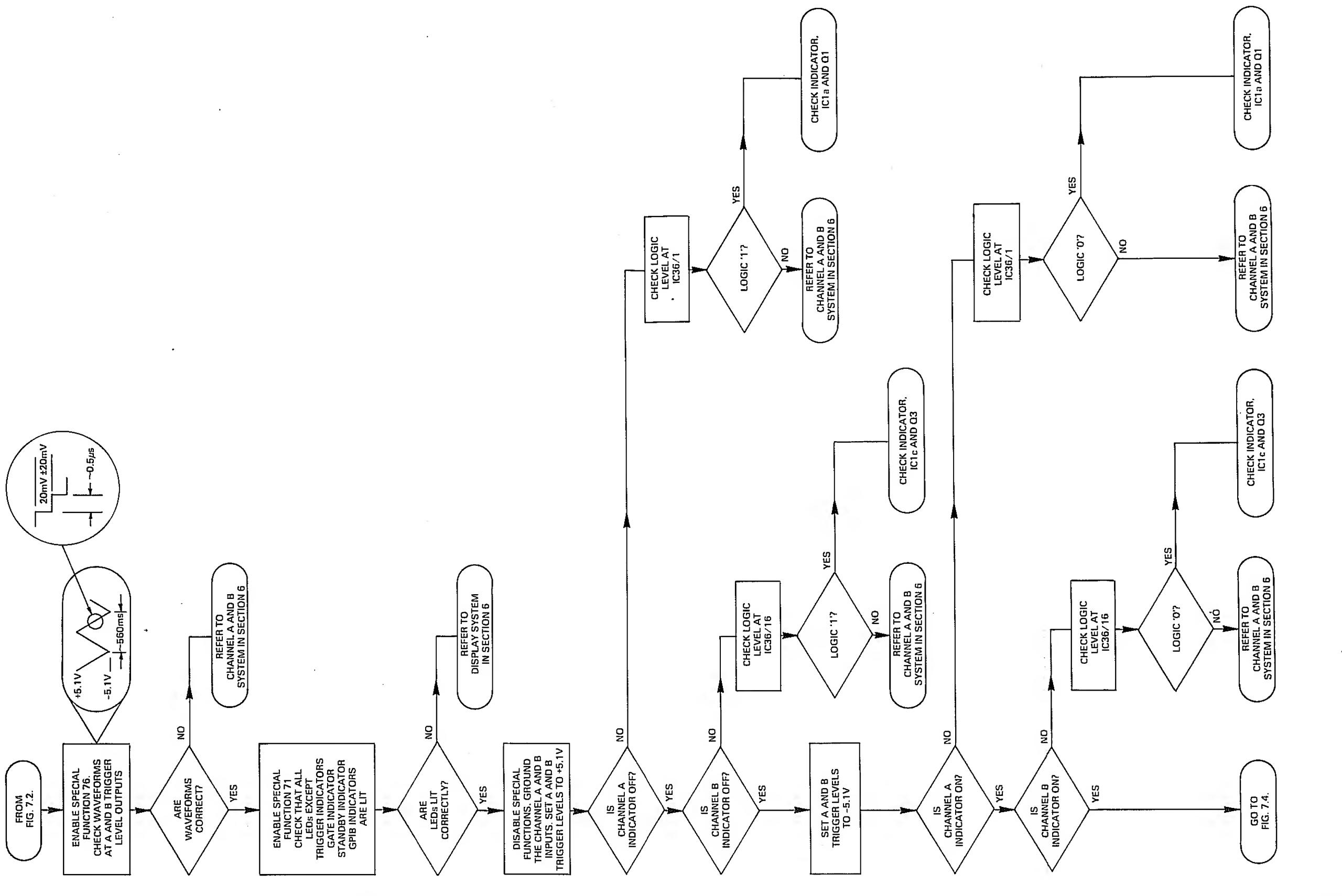


Fig 7.3 Fault Finding Flowchart - Part 3

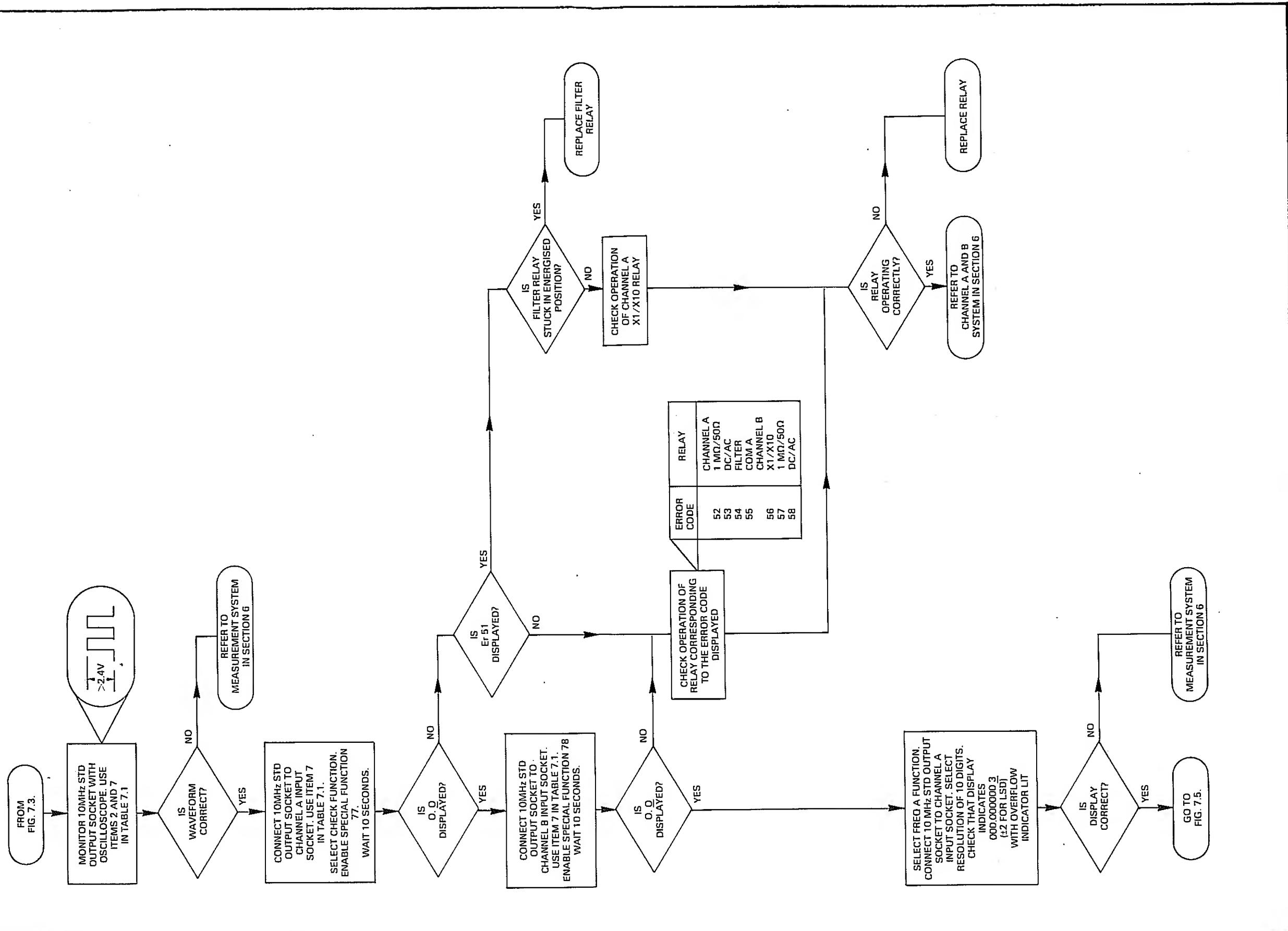


Fig 7.4 Fault Finding Flowchart - Part 4

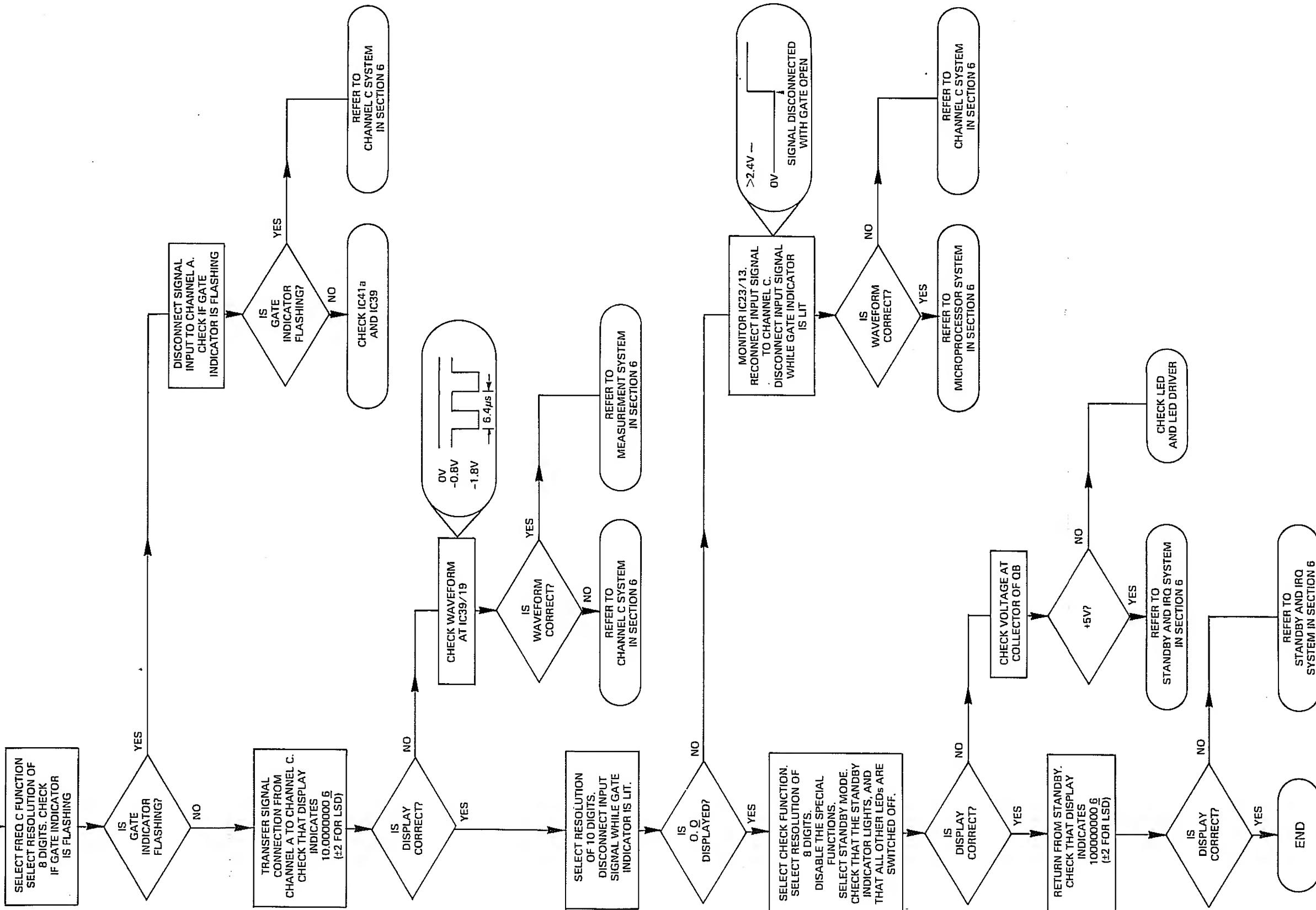


Fig 7.5 Fault Finding Flowchart - Part 5

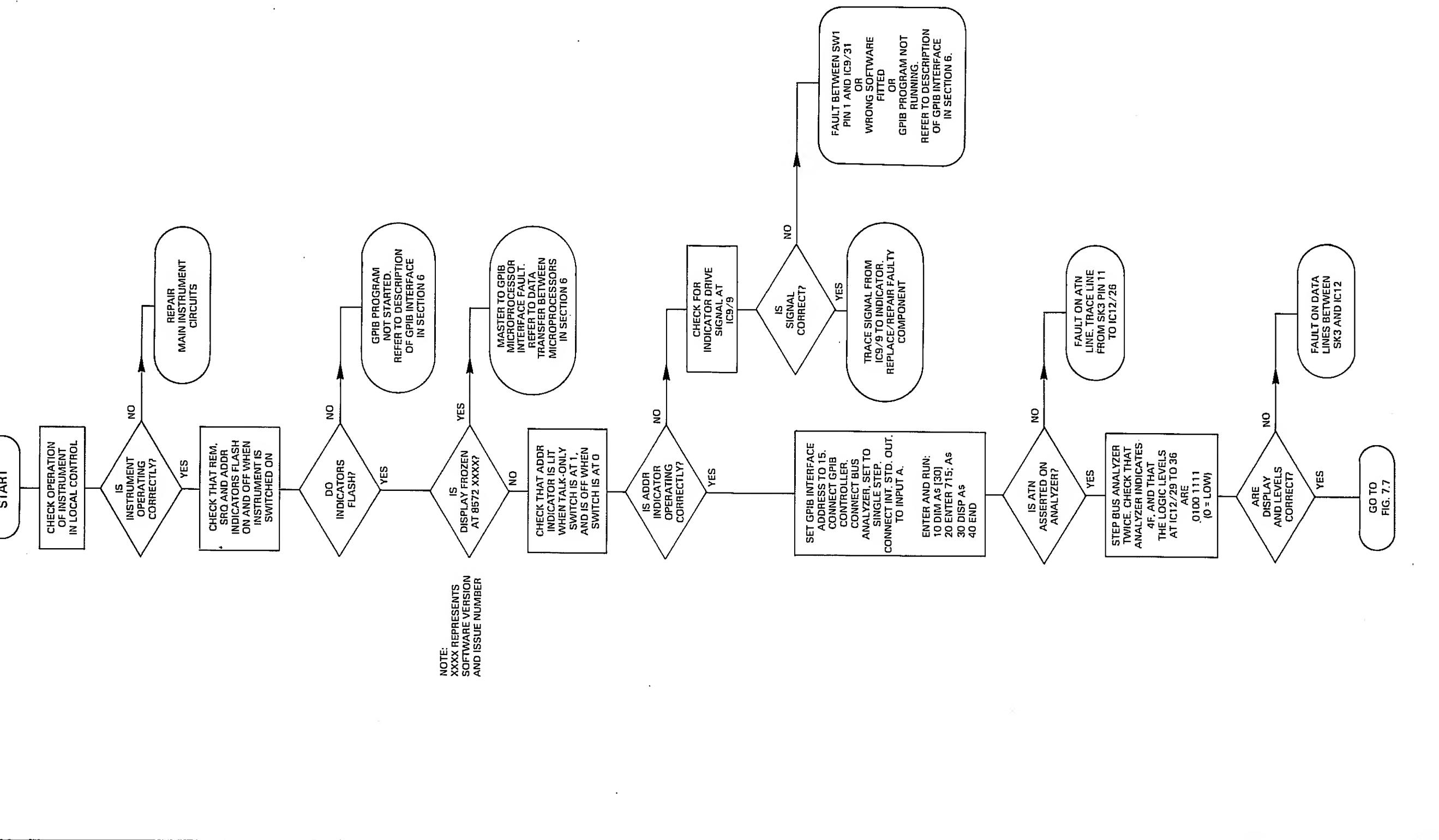
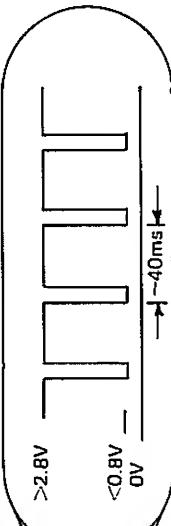


Fig 7.6 Fault Finding Flowchart - GPIB Part 1

MONITOR IC12/40
WITH OSCILLOSCOPE
PRESS AND HOLD
STEP KEY ON
BUS ANALYZER
SHOWN



CHECK THAT ADDR
INDICATOR LIGHTS
AND OSCILLOSCOPE
DISPLAYS WAVEFORM
SHOWN

IS INDICATOR ON
AND CORRECT
WAVEFORM
DISPLAYED?

RELEASE BUS
ANALYZER FROM
SINGLE STEP.
ALLOW CONTROLLER
TO RUN PROGRAM.
CHECK THAT
CONTROLLER DISPLAYS
FA + 00.00000 E + 06

INSTRUMENT NOT
ENTERING ADDRESSED-
TO-TALK STATE.
ABORT PROGRAM AND
DISPLAY GPIB ADDRESS.
(SEE SECTION 3 PARA 19)

CHECK ADDRESS
SWITCHES AND
LEVELS AT IC9/32 TO 36
OR
SUSPECT FAULT IN
IC12 OR IC9

IS
ADDRESS
15?

HANDSHAKE FAULT.
REFER TO WRITING
TO THE BUS IN
SECTION 6

IS
DISPLAY
CORRECT?

HANDSHAKE FAULT.
CHECK NRFD, DAV
AND NDAC LINES
BETWEEN SK3 AND IC12.
REFER TO WRITING TO
THE BUS IN SECTION 6

SEND
OUTPUT 715: "IPRS6"
CHECK THAT THE
REM AND ADDR
INDICATORS LIGHT
AND THE DISPLAY
INDICATES
10.00000 6

ARE
INDICATORS
AND DISPLAY
CORRECT?

SEND
OUTPUT 715: "XX"
CHECK THAT SRQ
INDICATOR LIGHTS

IS
INDICATOR
LIT?

IS SRQ
ACTIVE ON
BUS ANALYZER

TRACE SRQ LINE
FROM IC12/23 TO
SK3 PIN 10

GO TO
FIG. 7.8

CHECK THAT INSTRUMENT
IS IN REMOTE BY
PRESSING ANY FRONT
PANEL KEY

DOES
INSTRUMENT
RESPOND?

TRACE REN LINE
FROM SK3 PIN 17 TO
IC12/22

HANDSHAKE FAULT
CHECK NRFD, DAV AND
NDAC LINES BETWEEN
SK3 AND IC12.
REFER TO READING FROM
THE BUS IN SECTION 6

Fig 7.7 Fault Finding Flowchart - GPIB Part 2

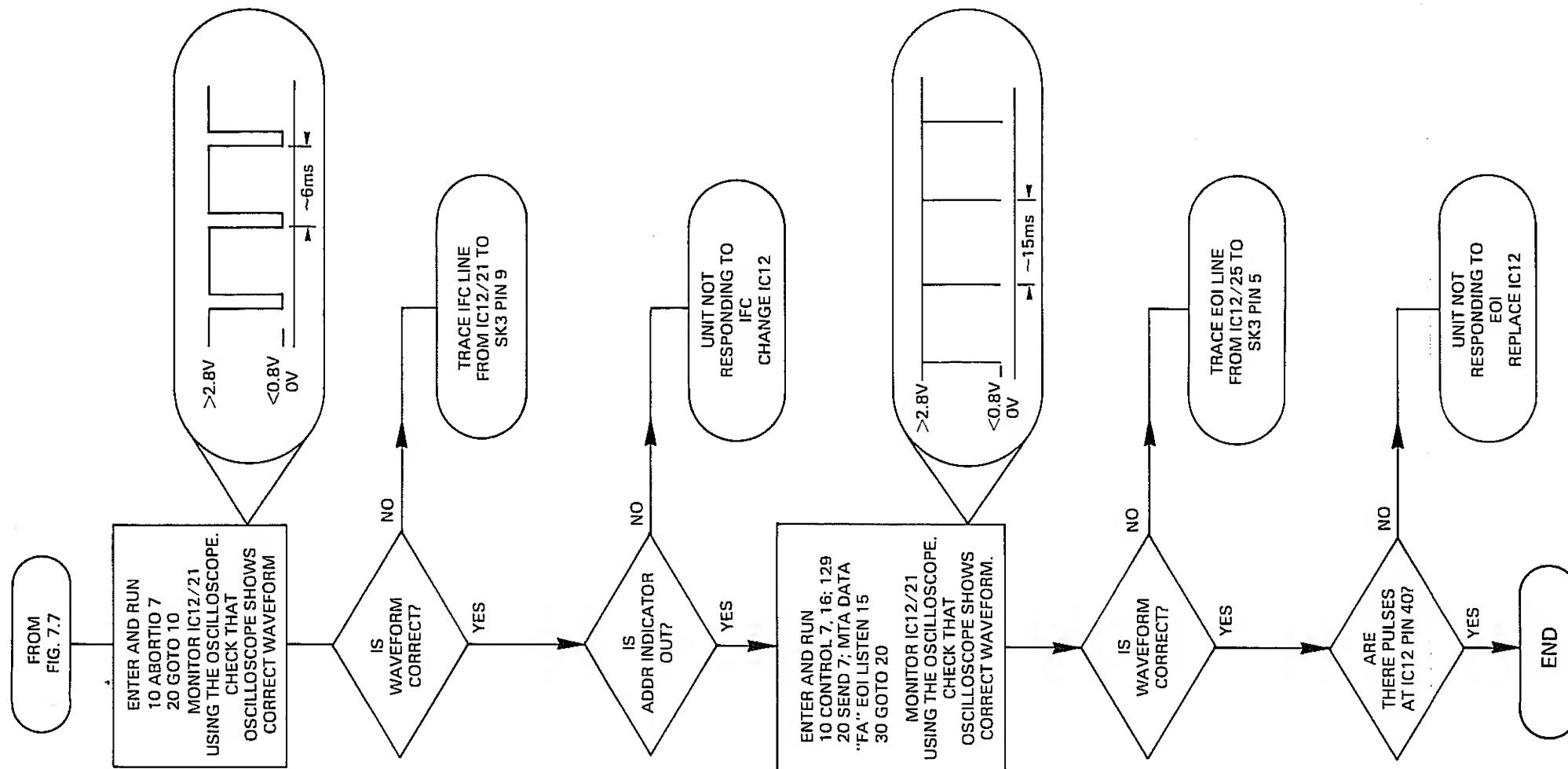
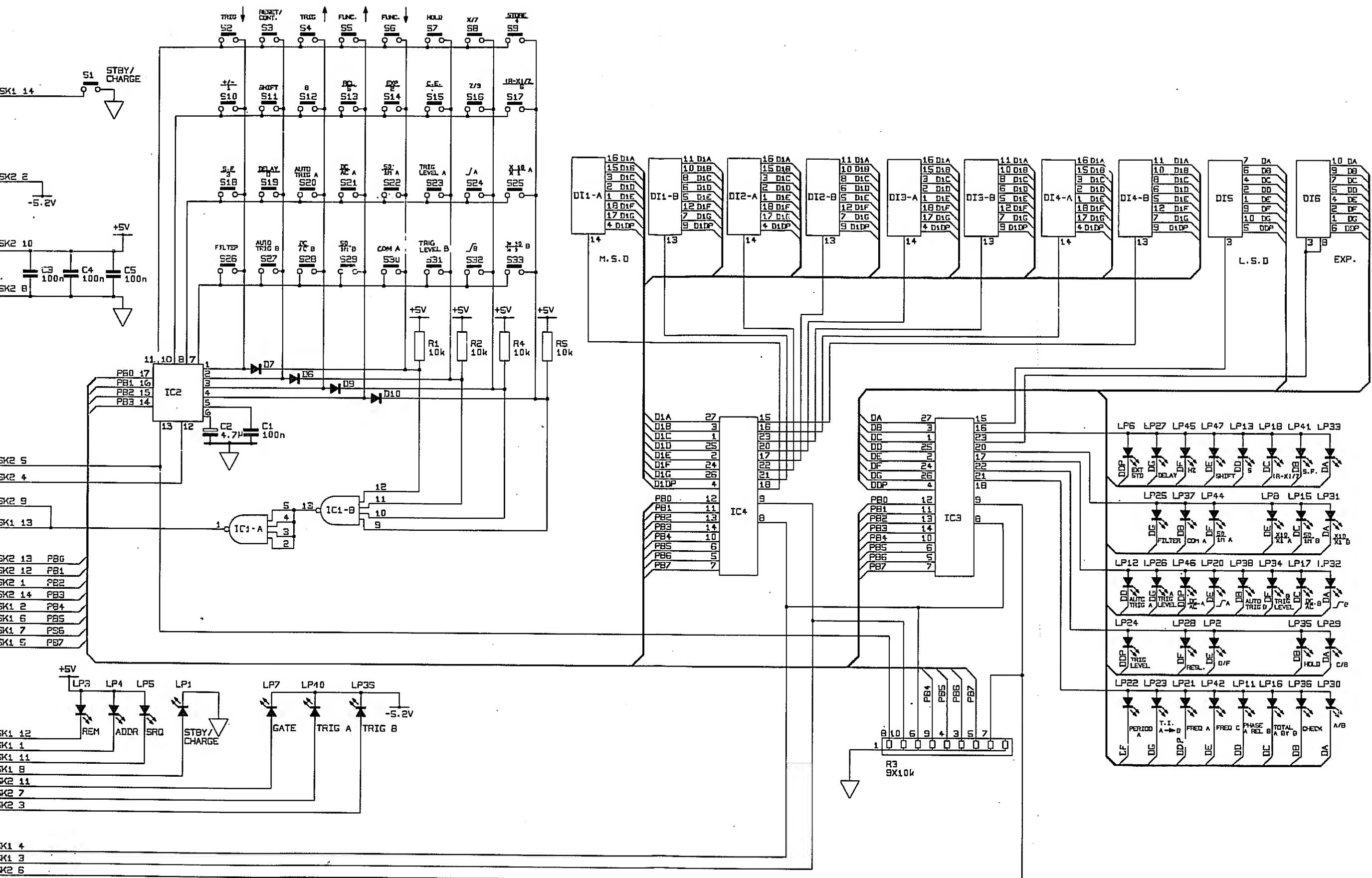
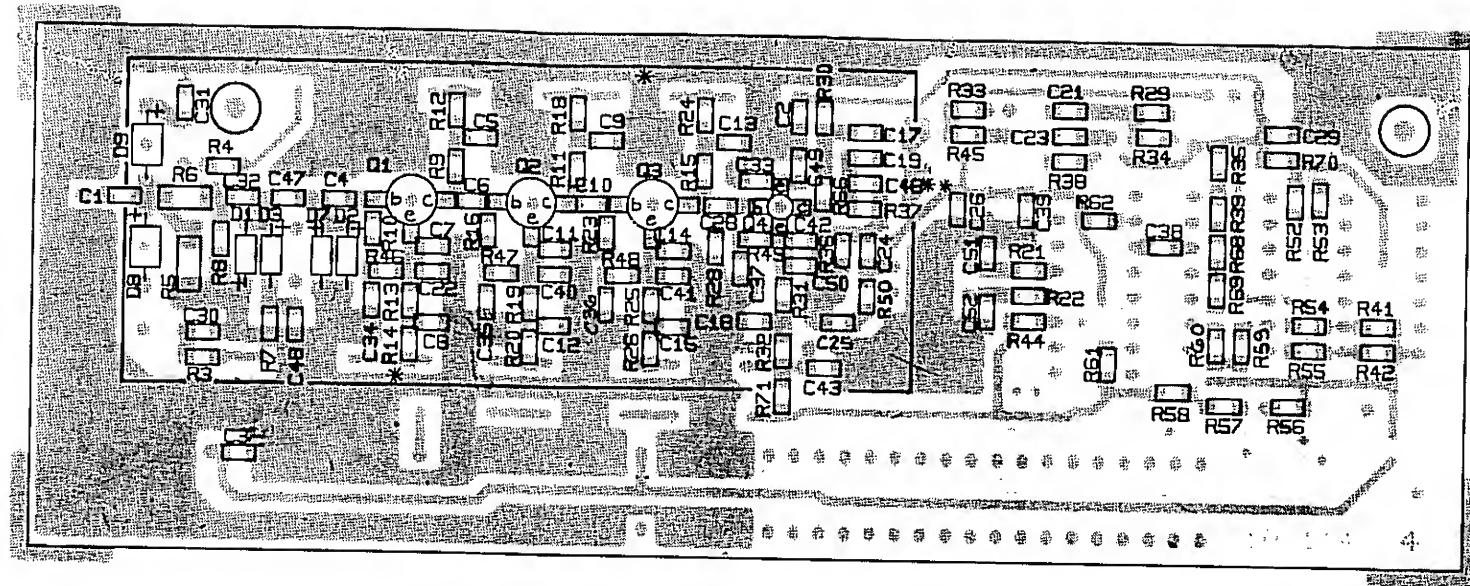


Fig 7.8 Fault Finding Flowchart - GPIB Part 3

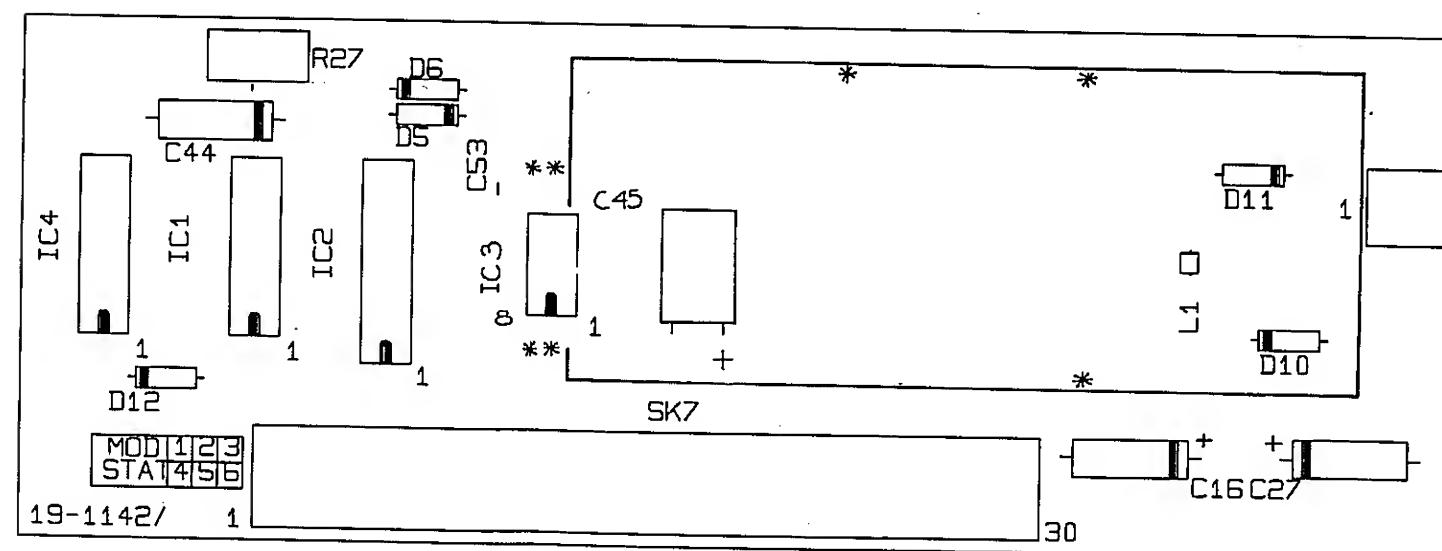


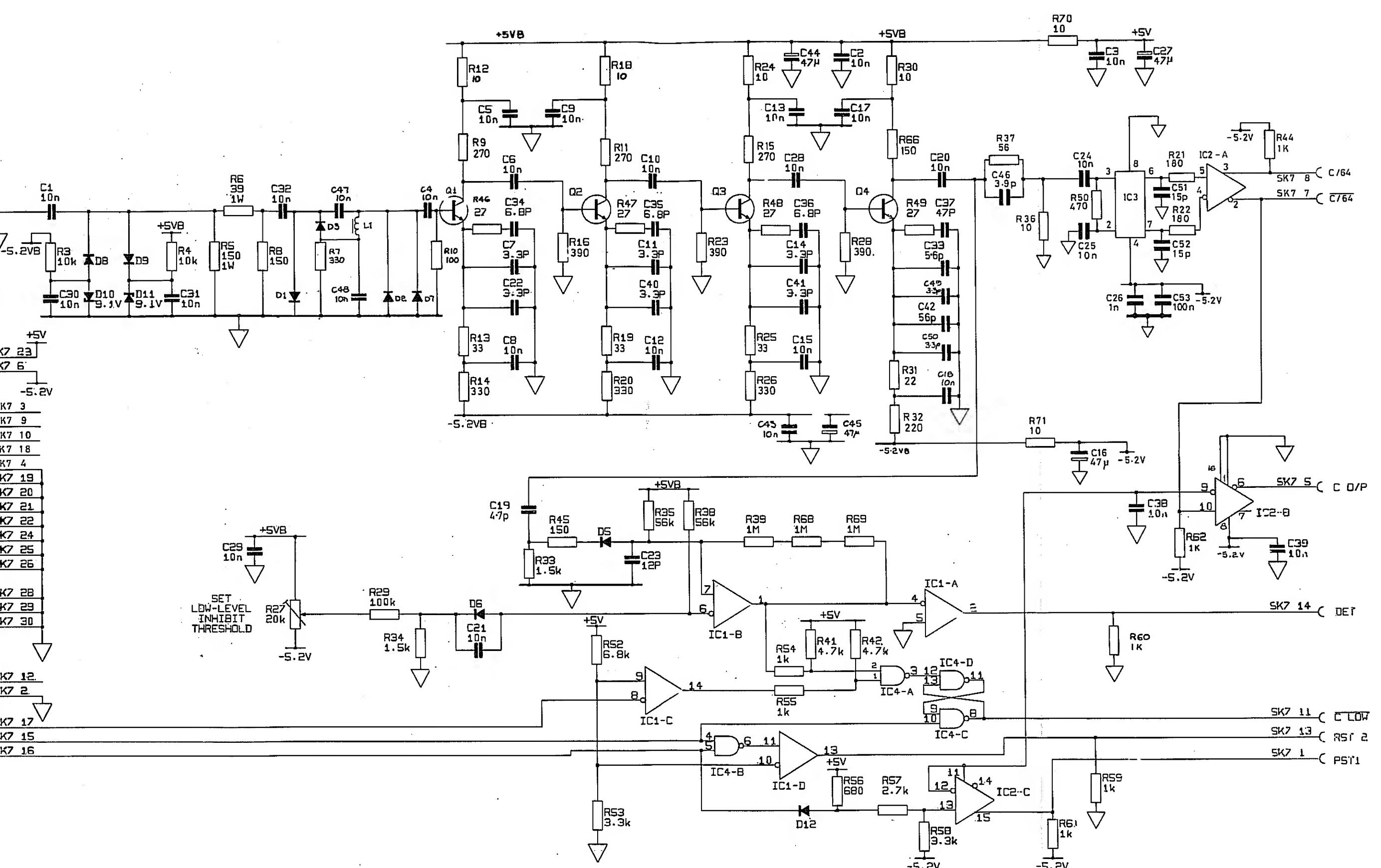
Circuit Diagram: Display Board Assembly 19-1141 Fig.3

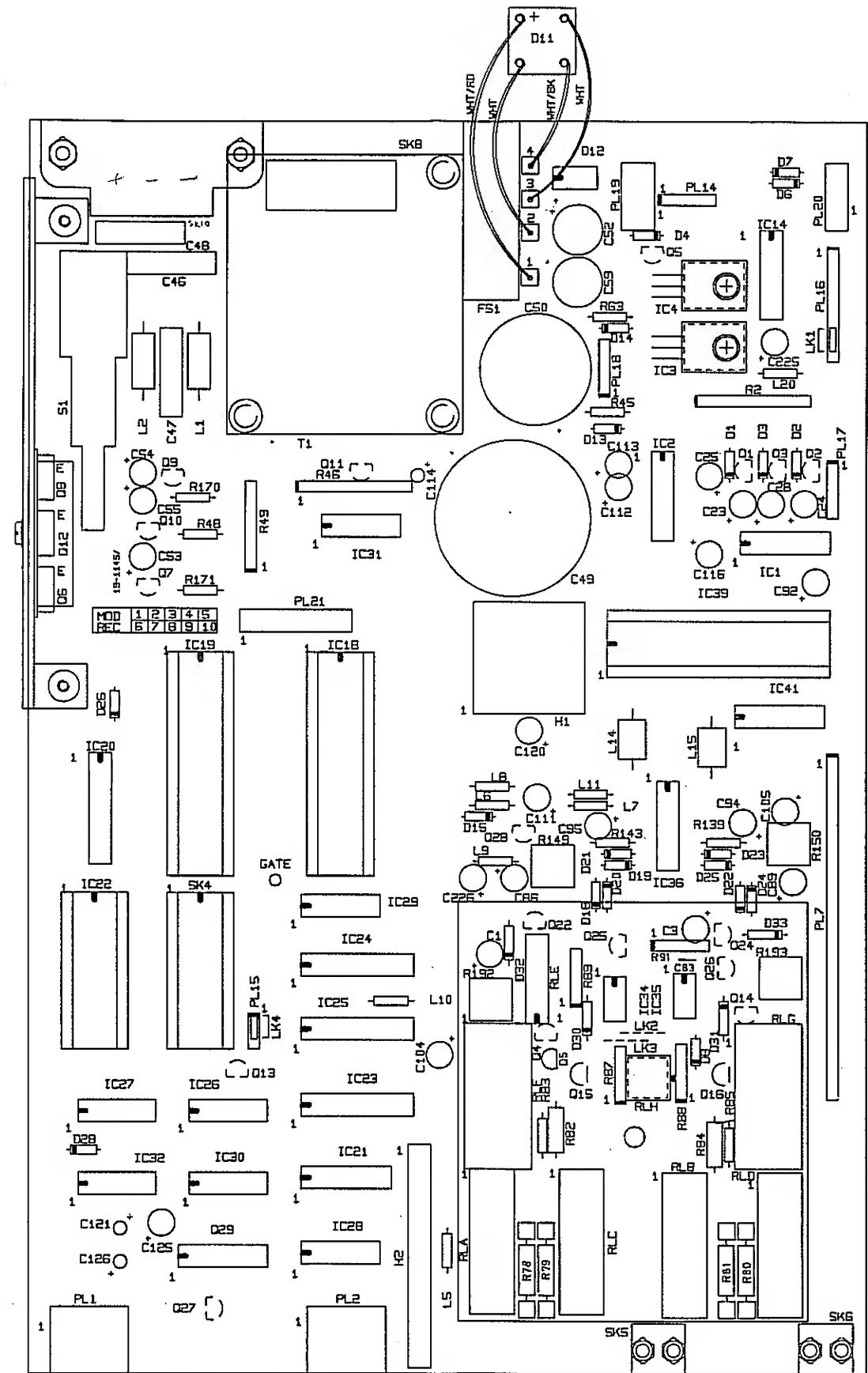
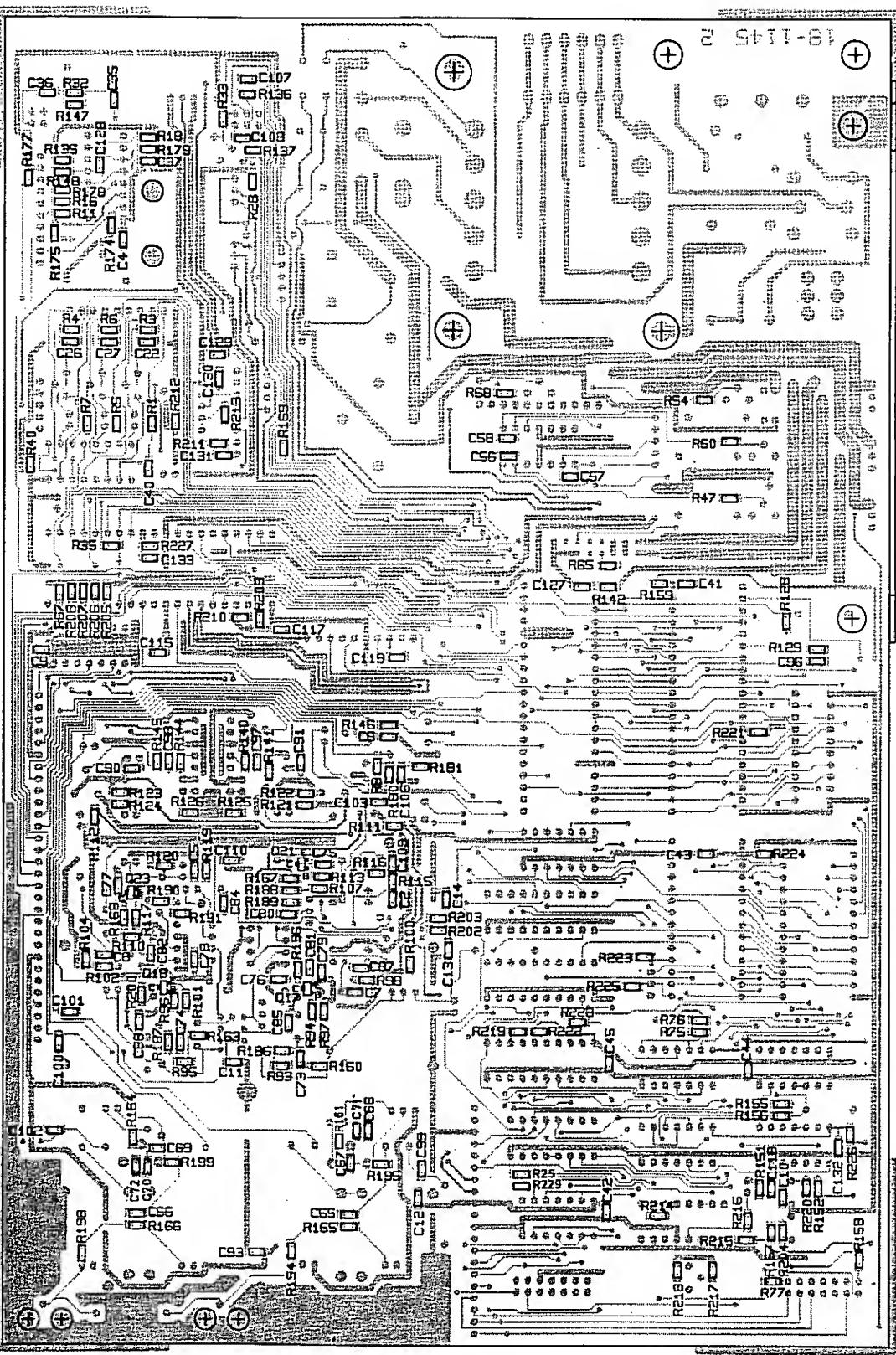
TRACKSIDE VIEW



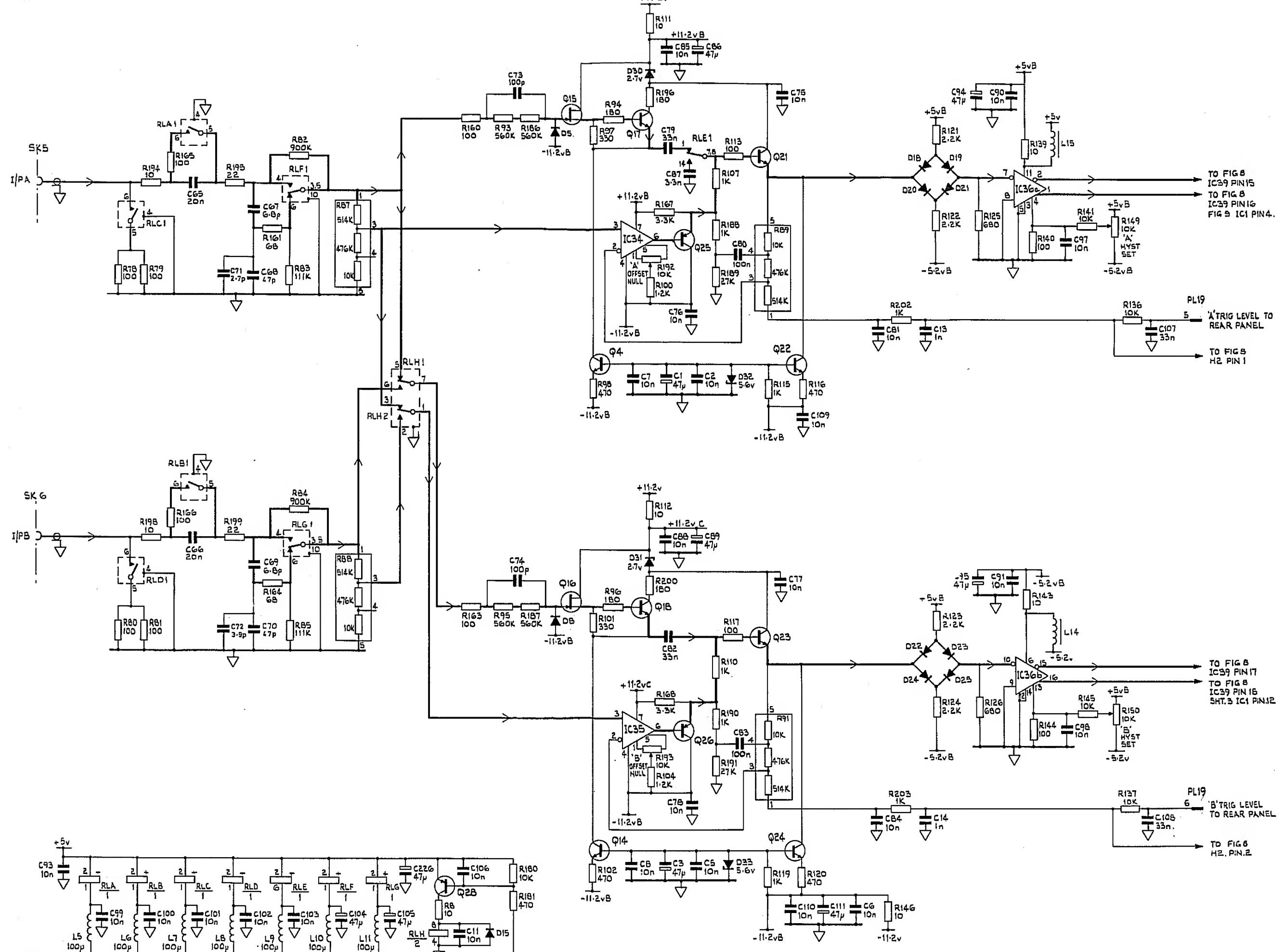
COMPSIDE VIEW







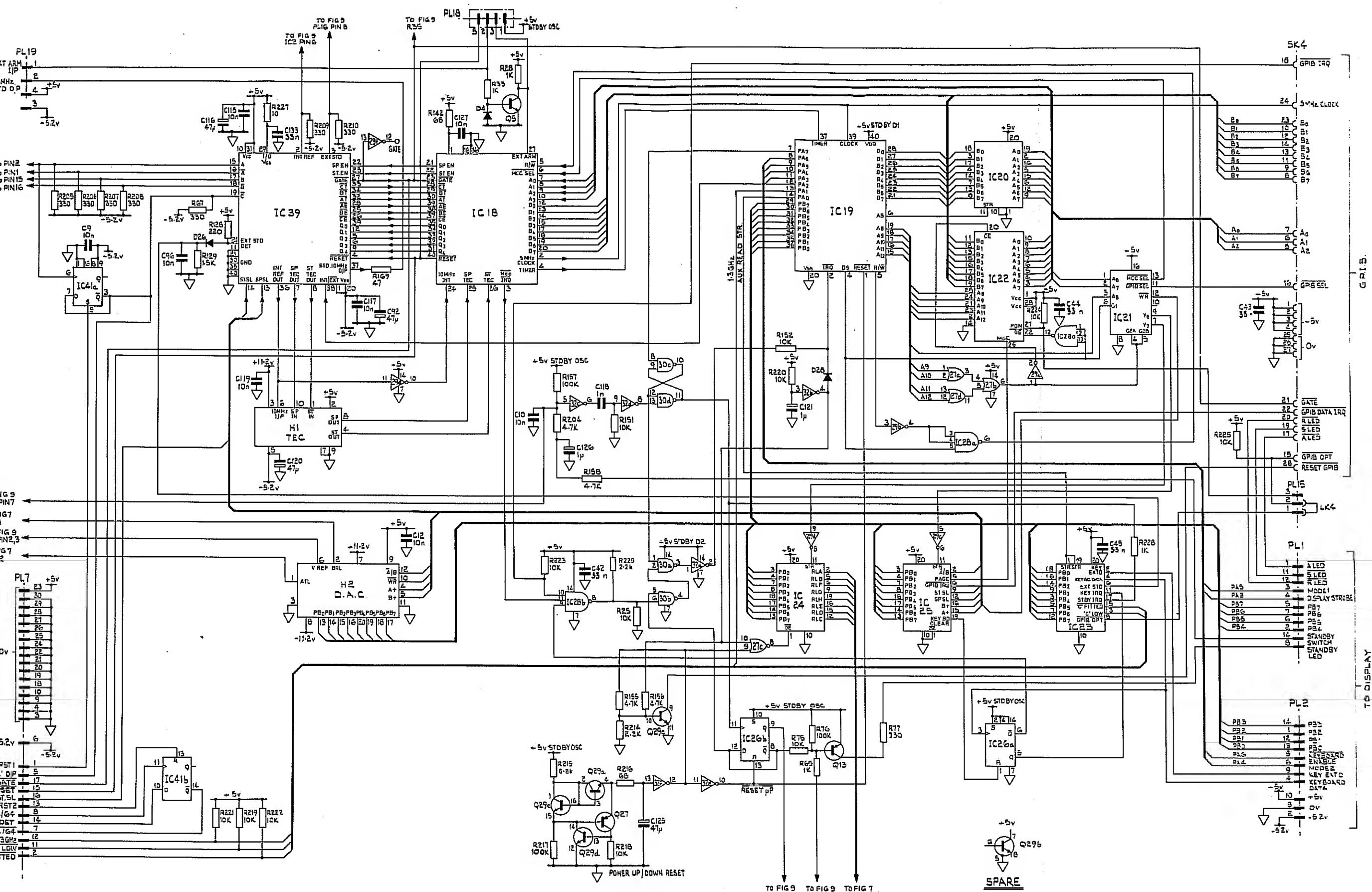
Component Layout:
Motherboard Assembly 19-1145 Fig-6



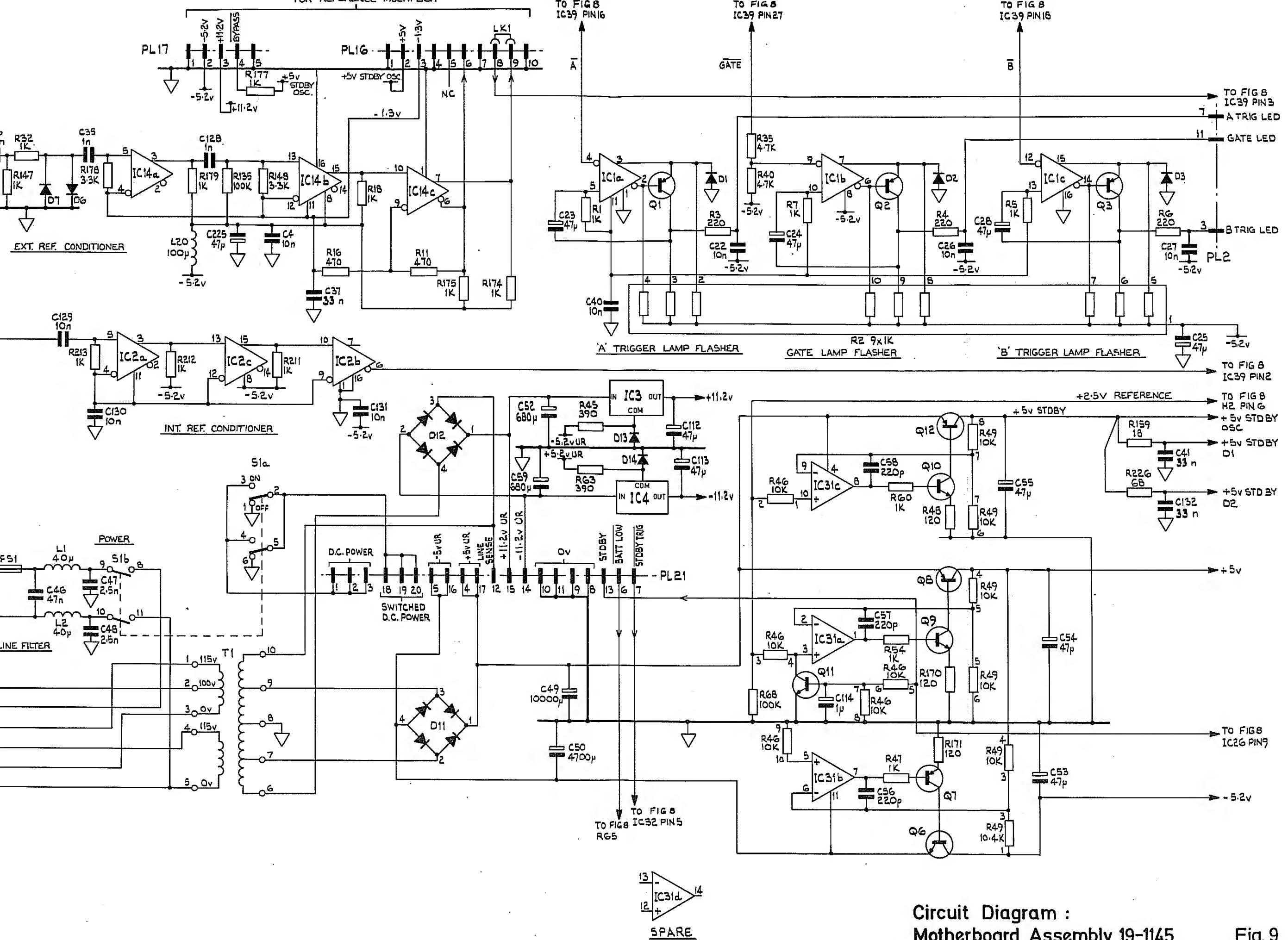
Circuit Diagram :

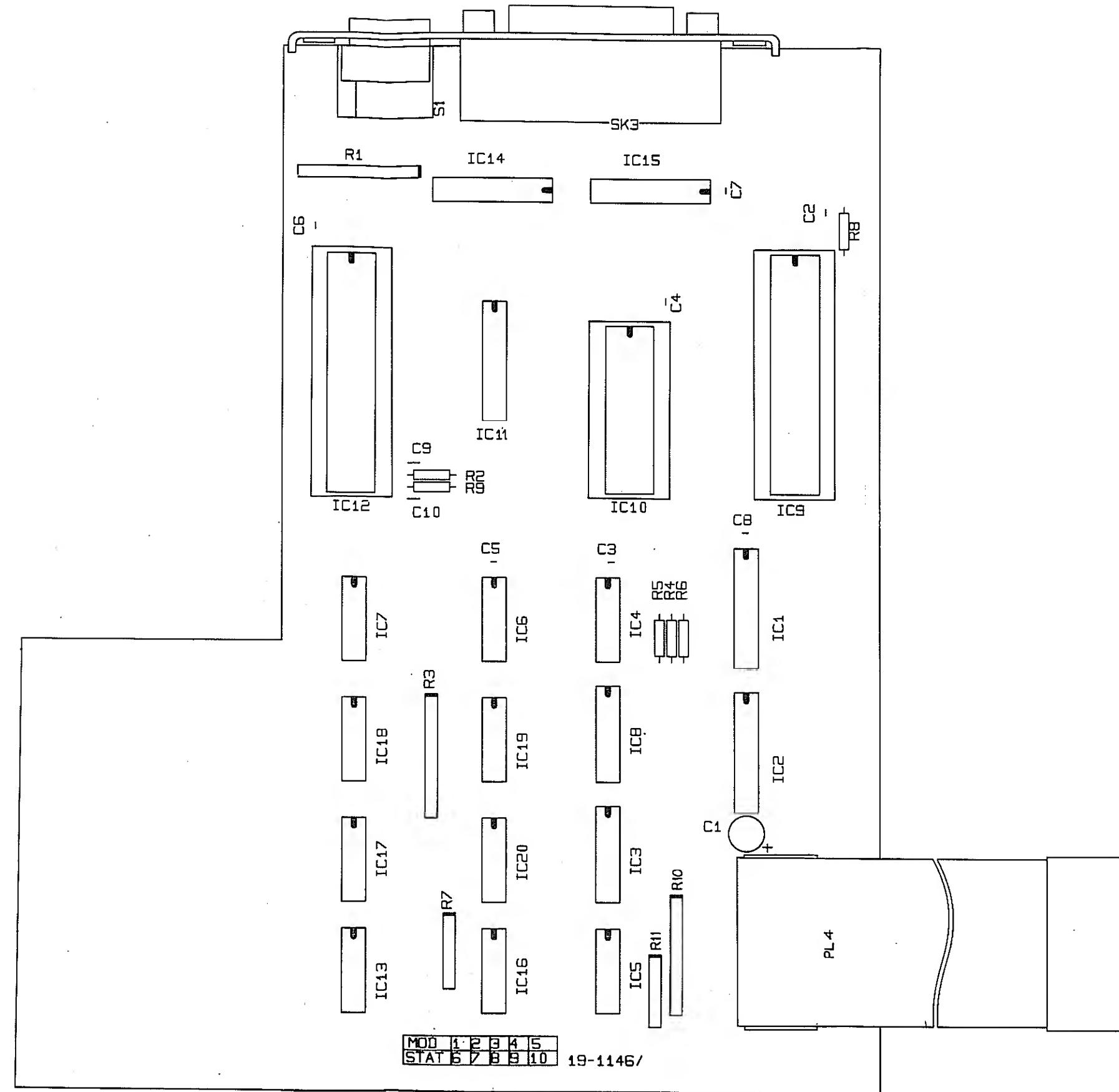
Motherboard Assembly 19-1145

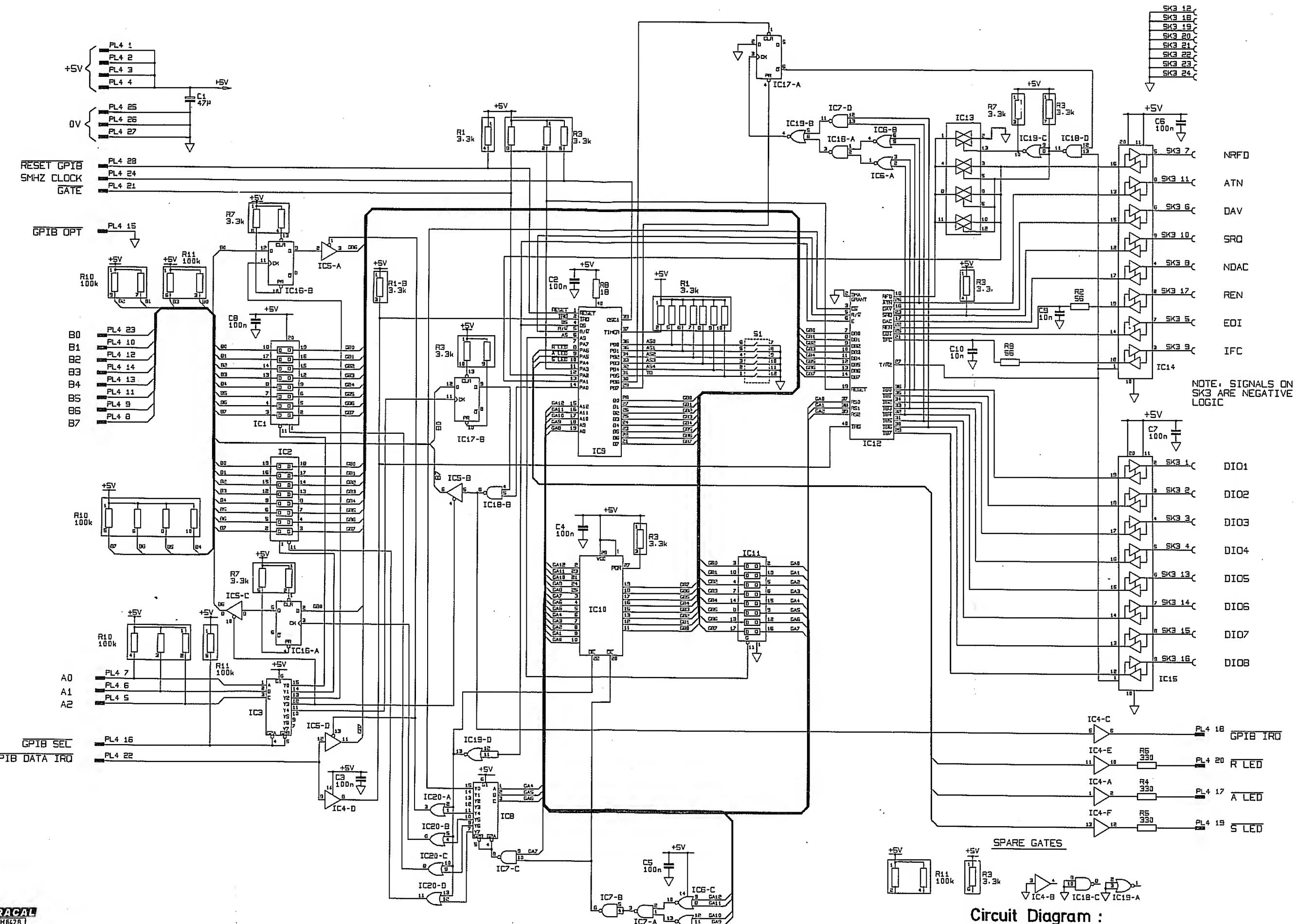
Fig 7



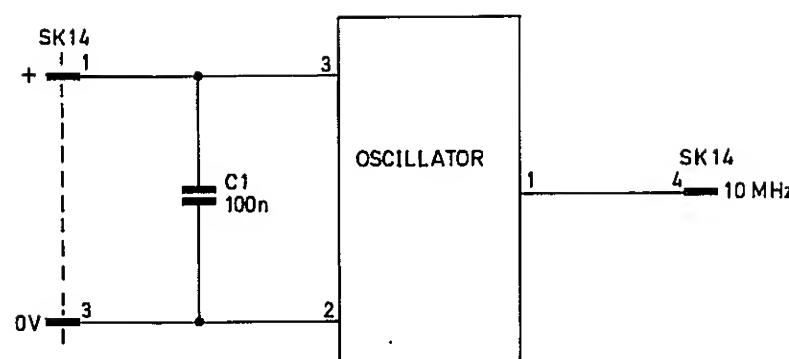
Circuit Diagram :
Motherboard Assembly 19-1145





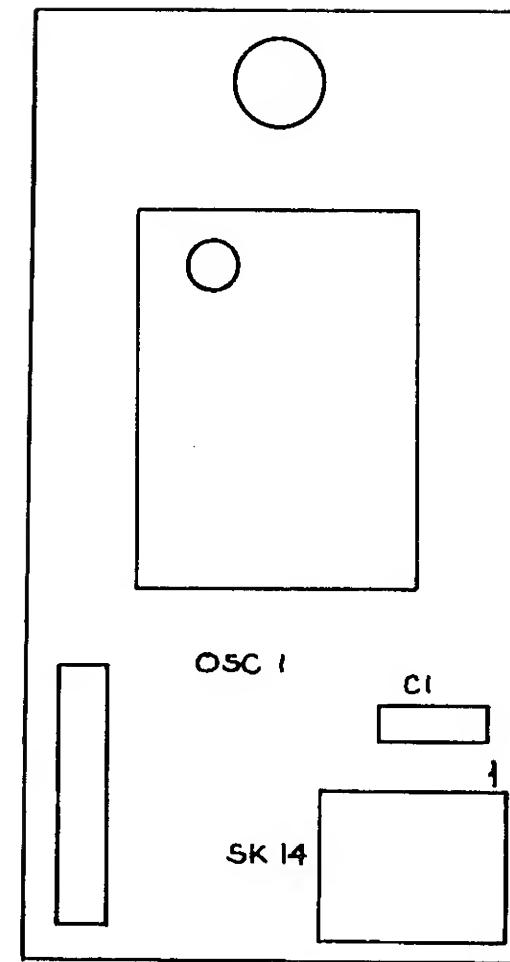


**Circuit Diagram :
GPIB Assembly 19-1146**



Circuit Diagram
Oscillator Assembly 19-1147

Fig.13

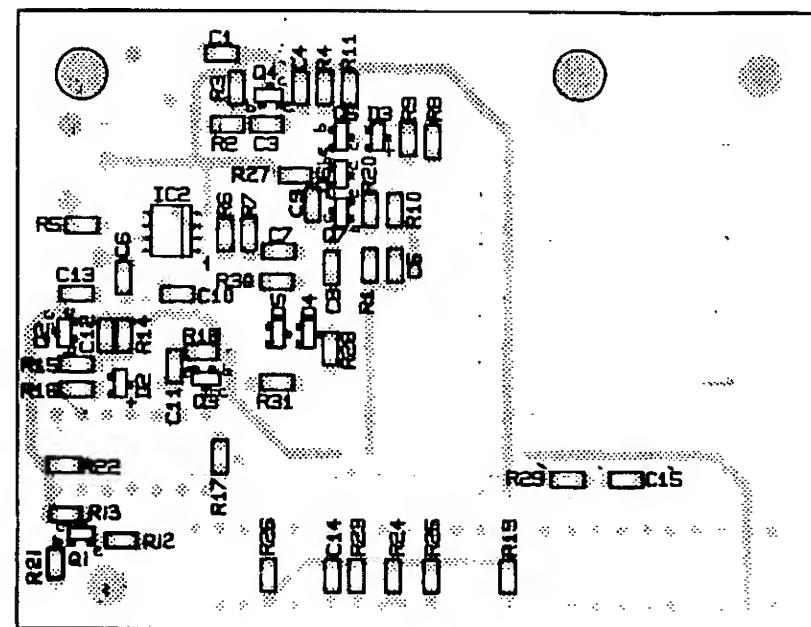


Component Layout:
Oscillator Assembly 19-1147

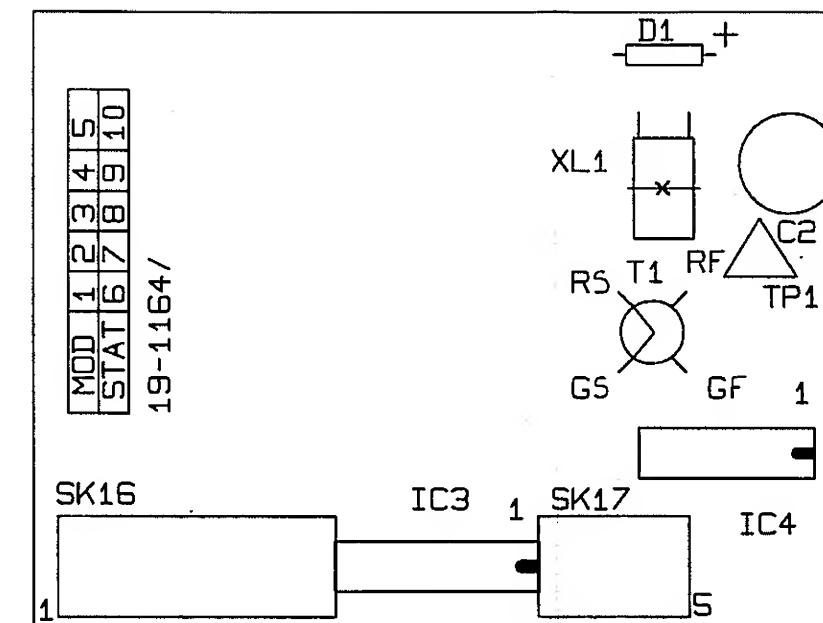
RACAL
TH 6284
1

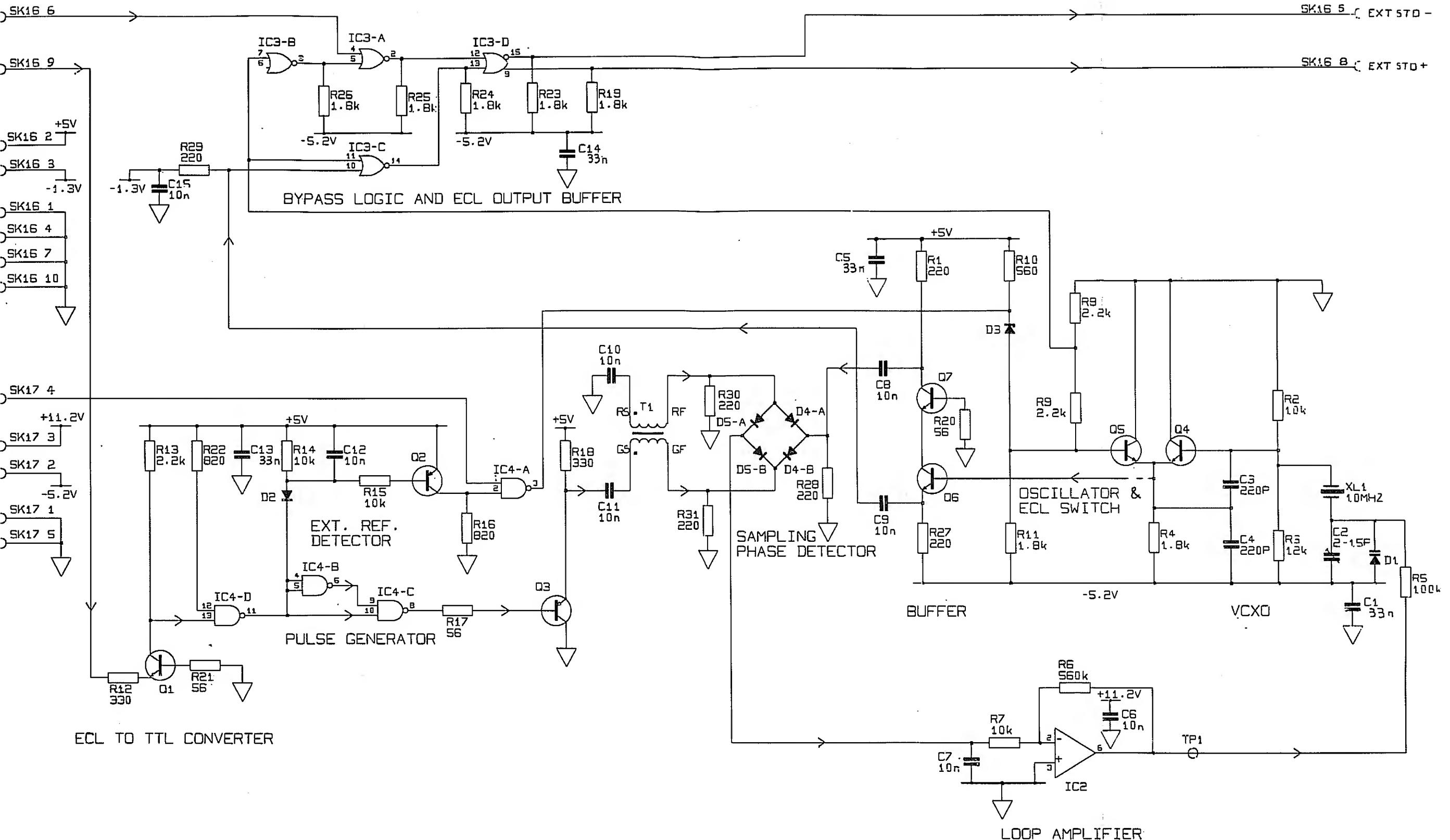
Fig.12

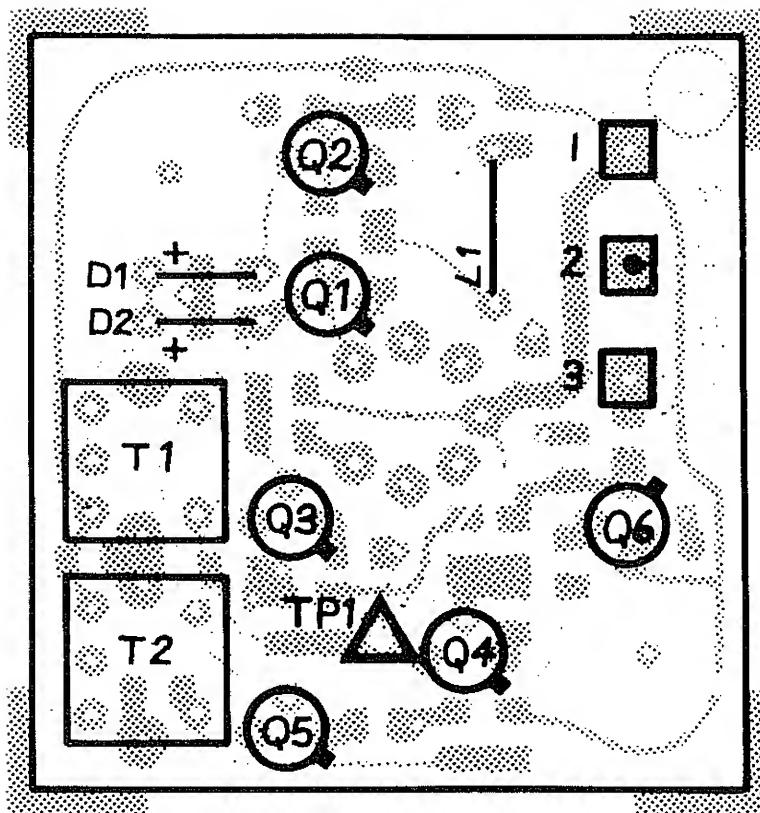
TRACKSIDE VIEW



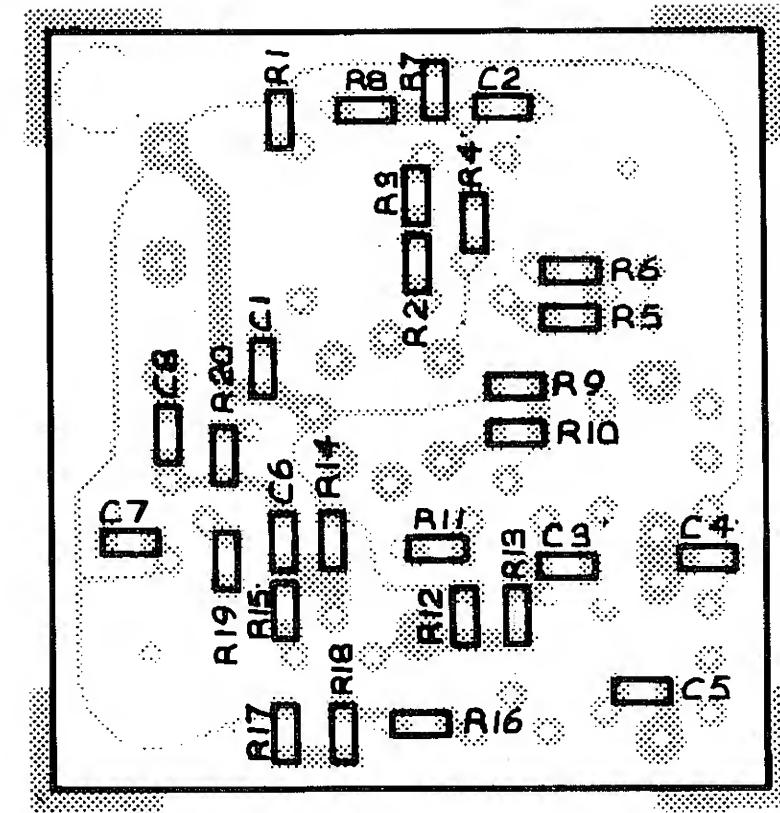
COMPONENT SIDE VIEW



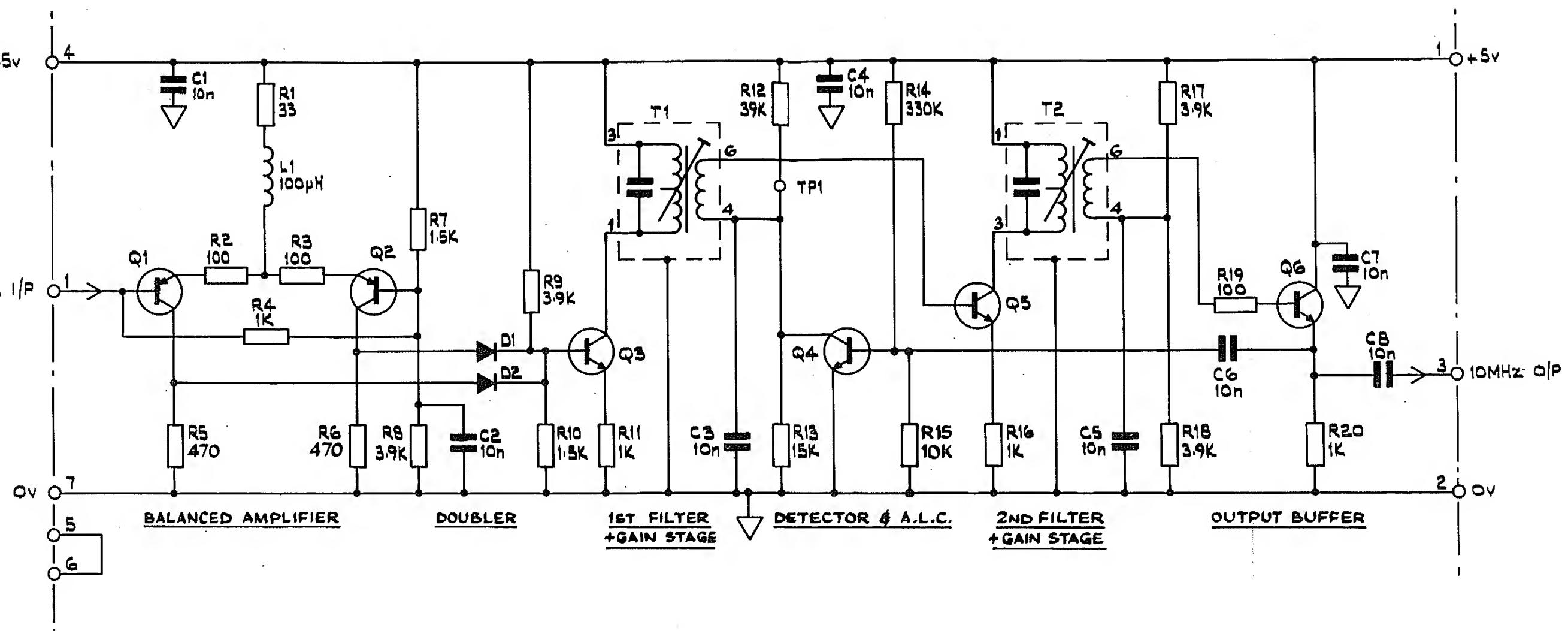




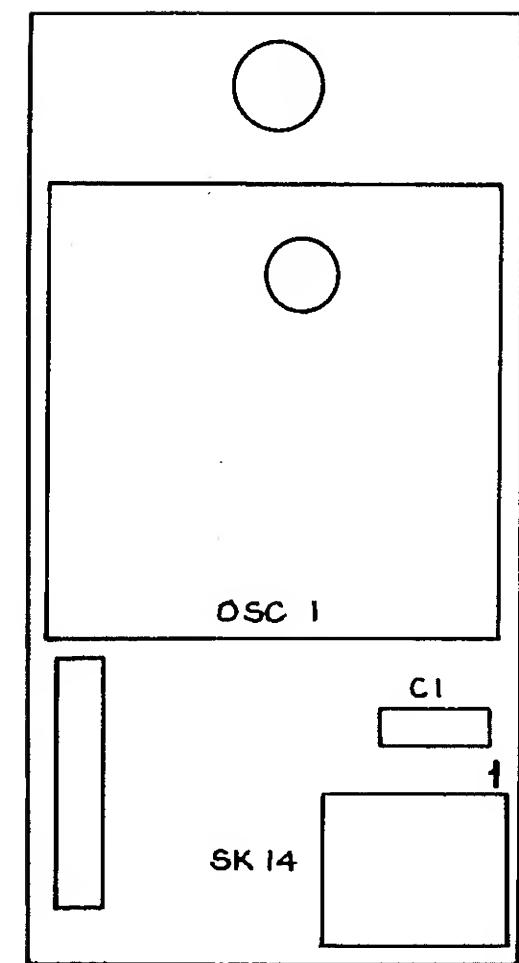
VIEWED FROM COMPONENT SIDE



VIEWED FROM TRACK SIDE



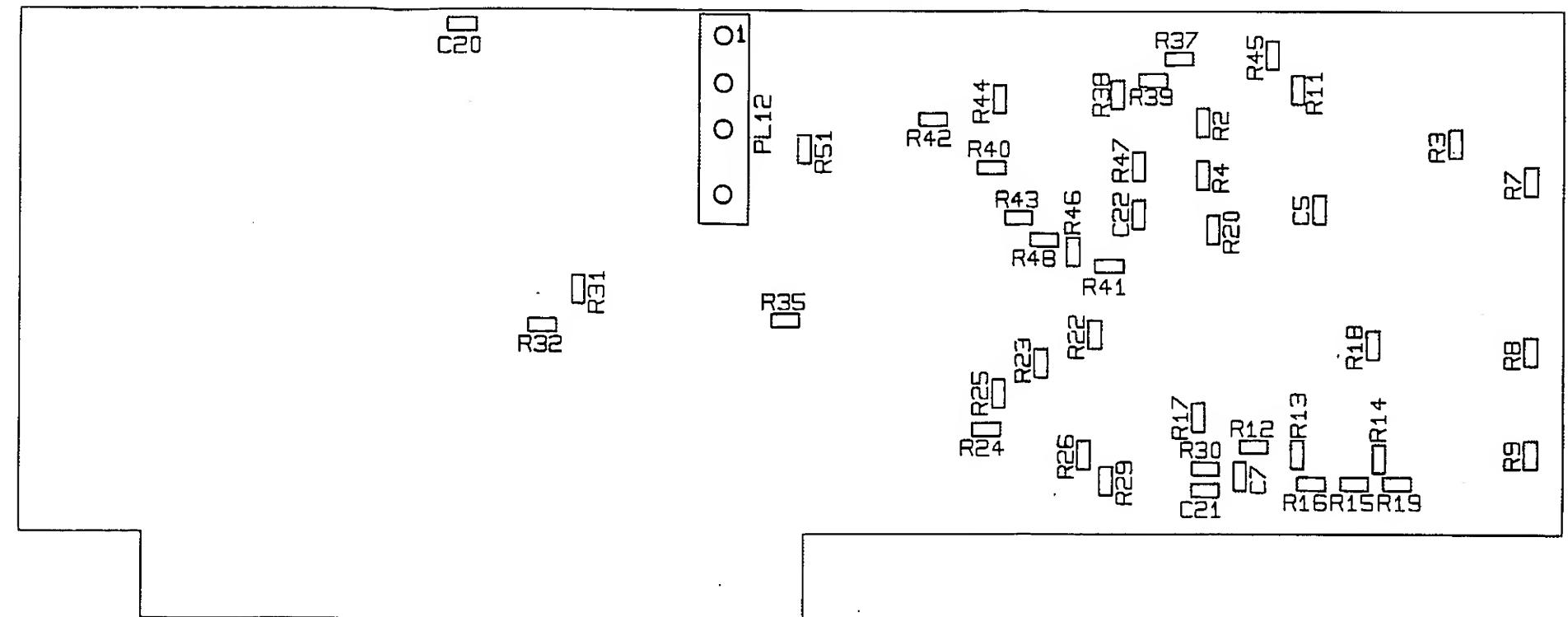
Circuit Diagram: Reference
Frequency Doubler Assembly 19-1238 Fig.17



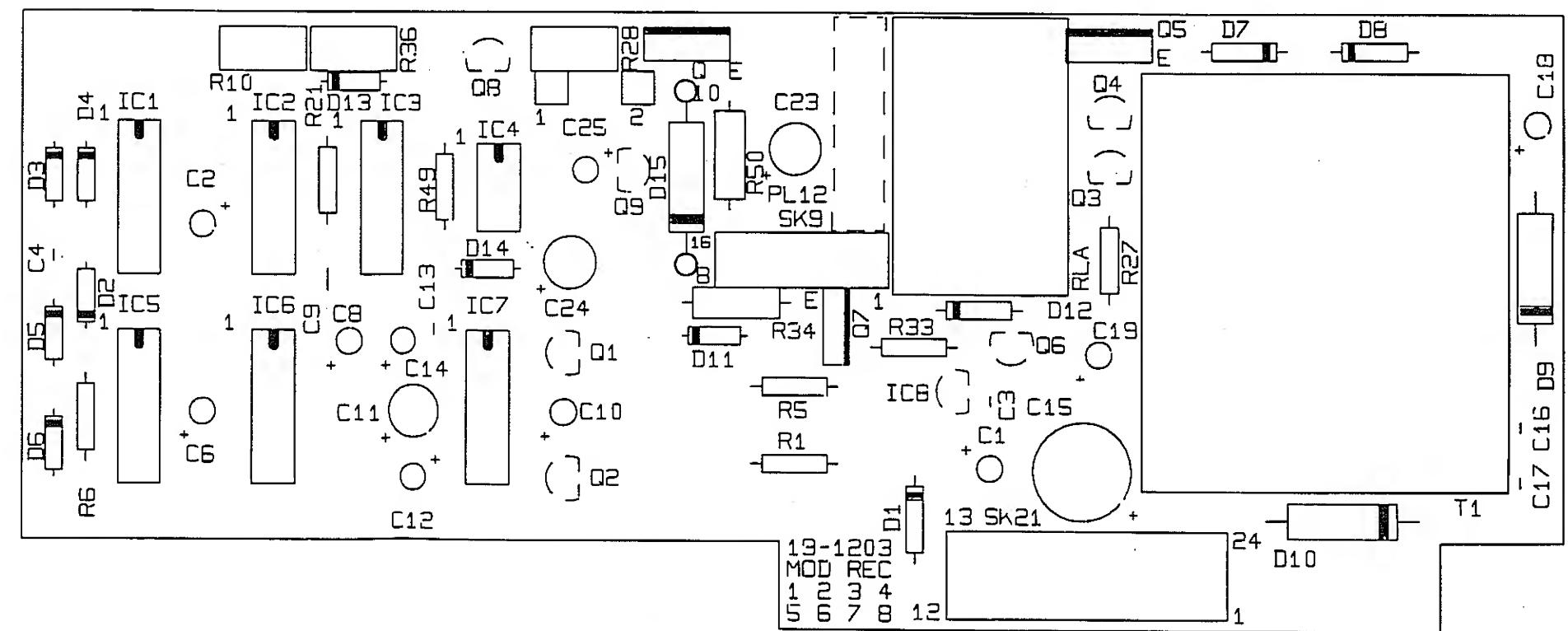
RACAL
TH 6284
1

Component Layout:
Oscillator Assembly 19-1208

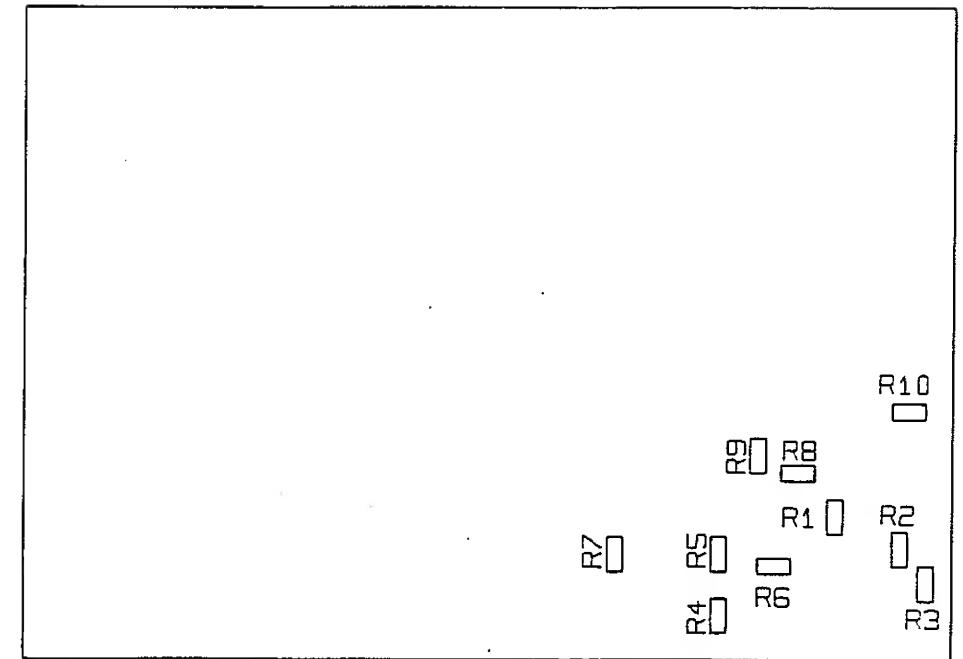
Fig.18



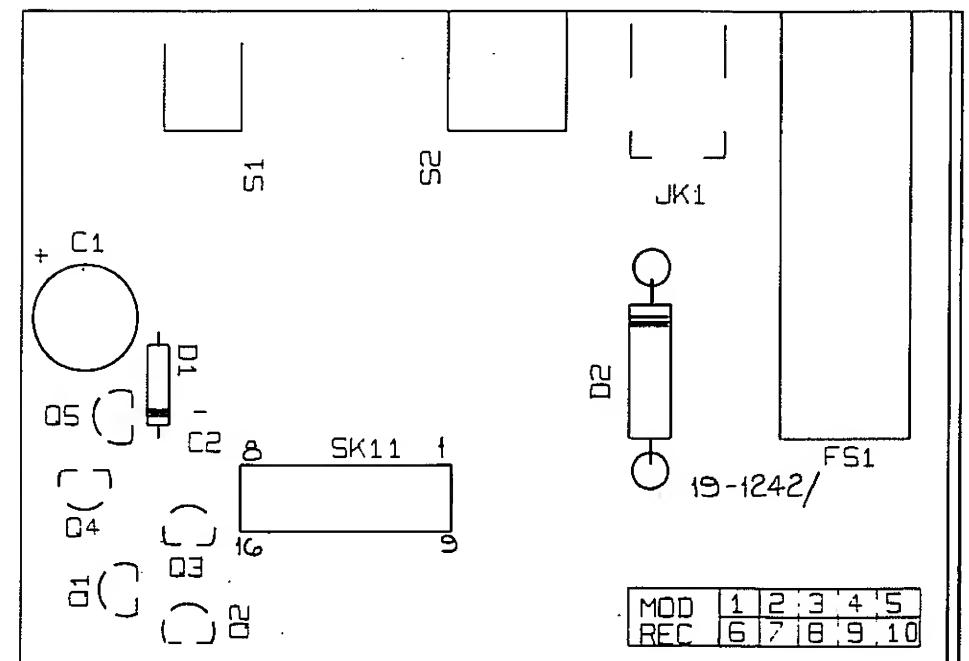
TRACKSIDE VIEW



COMPONENT SIDE VIEW



TRACKSIDE VIEW

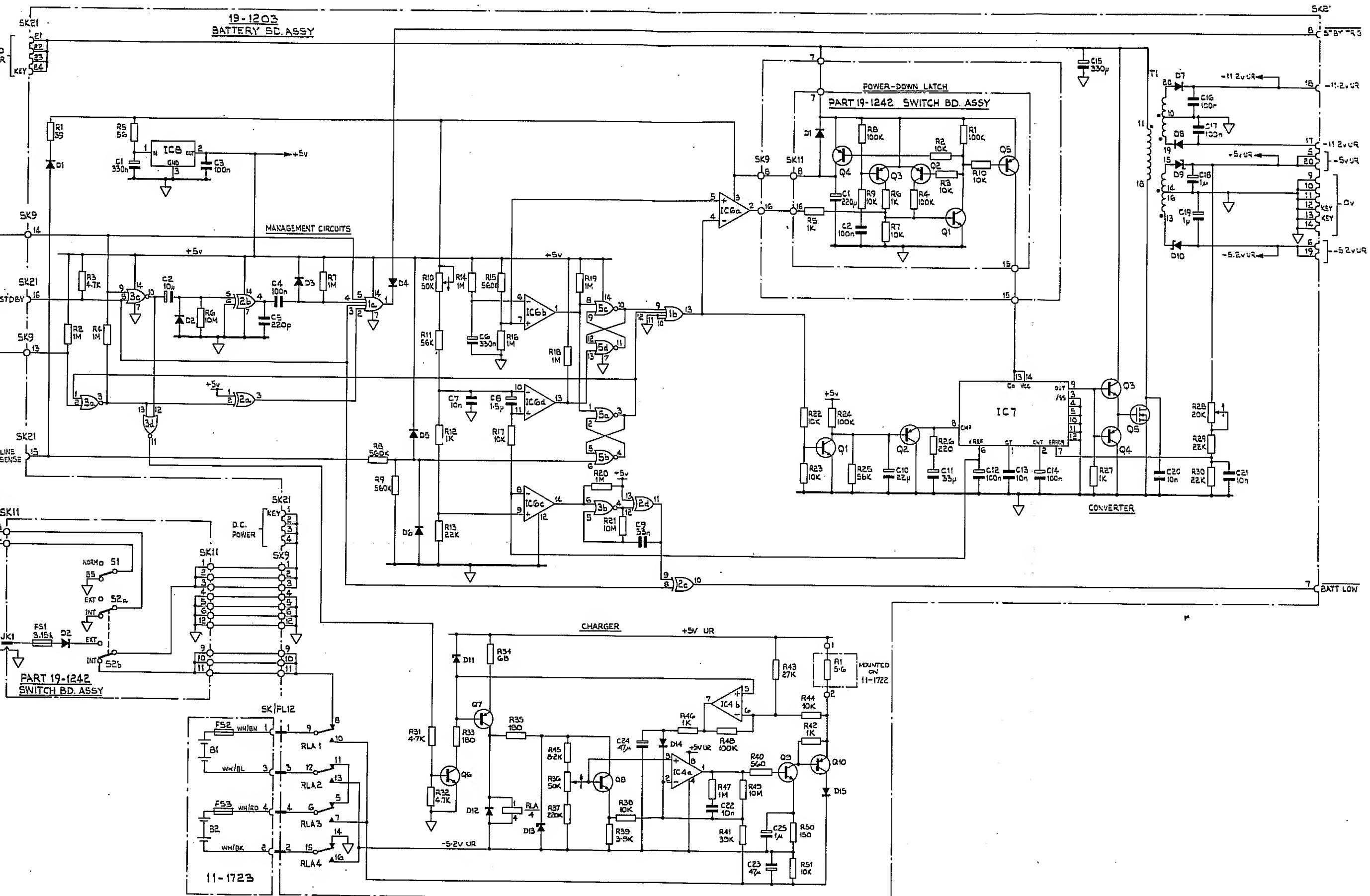


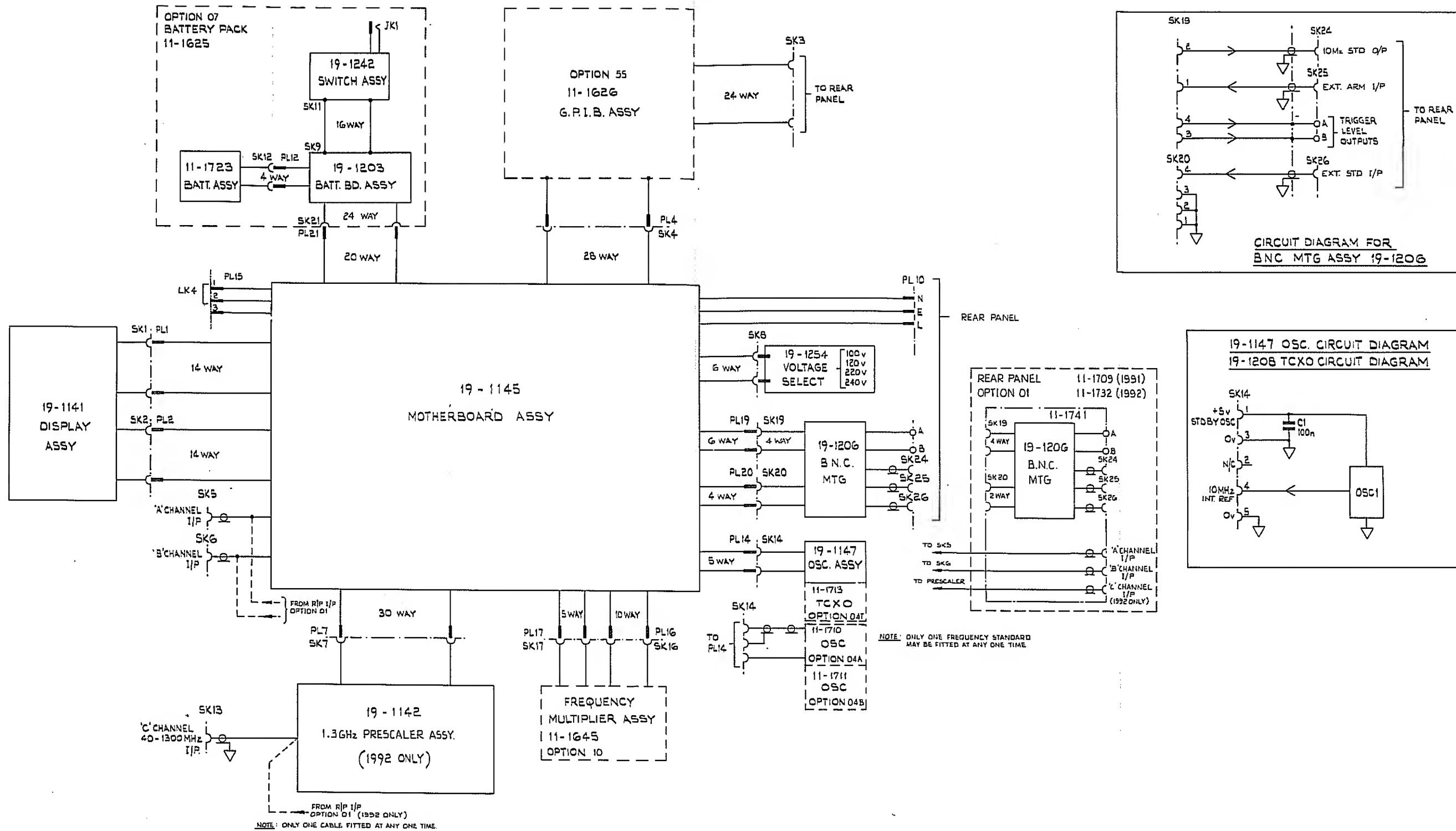
COMPONENT SIDE VIEW

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TH 6428

Component Layout:

Fig. 20





	PL 1	SK 1
PIN	PIN	
14	14	
TEND		13
2	2	
6	6	
7	7	
5	5	
12	12	
1	1	
11	11	
8	8	
4	4	
3	3	

14 WAY	PL/5W
	2
PIN	
-5V	2
0V	8
+5V	10
KEYBOARD ENABLE	5
KEYBOARD DATA	4
KEYBOARD EXTEND	9
PBO	13
PB1	12
PB2	1
PB3	14
GATE	11
A TRIG	7
B TRIG	3
MODE 2	6

24 WAY		SK3
		PIN
DV		12,16,19,20
		21,22,23,24
NRFD		7
ATN		11
DAV		6
SRO		10
NDAC		5
REN		17
EO1		5
1FC		9
DI01		1
DI02		2
DI03		3
DI04		4
DI05		13
DI06		14
DI07		15
DI08		16

26 WAY	SK / A
+5v	1, 2, 3,
0v	25, 26,
RESET	25
GP1B	24
SMHz	CLOCK
GATE	2!
GP1B SOT	15
B0	23
B1	10
B2	12
B3	14
B4	13
B5	11
B6	9
B7	8
A0	7
A1	6
A2	5
GP1B SEL	1G
GP1B DATA, INC	22
GP1B IRQ	18
R1LED	20
AL1ED	17
S1LED	19

4 WAY	PLISK
	12
	PIN
BATTERY 1 OV	3
BATTERY 1 +6V	1
BATTERY 2 +6V	4
BATTERY 2 OV	2

30 WAY	PL7	SK7 1522-2
PIN	PIN	PIN
+5V	23	23
-5V	6	6
0V	5, 9, 10, 10, 12, 14-15, 16-20	4, 15- 16, 18
1.3 GHz	12	12
GATE	17	17
RESET	15	15
ST. SL or U	16	16
C764	8	8
C766	7	7
C1/O	5	5
DET	14	14
C LOW	11	11
RST2	13	13
PST1	1	1
C FITTED	2	2

16 WAY	SK9, 11
	PIN
CV	4, 5, 6, 12
NORM/BS	14
EXT/INT	13
POWER EXT/INT	1, 2, 3
BATTERY	9, 10, 11

5 WAY	PL/ SK
	14
CV	PIN
+5V STBY OSC	1
CV	3, 5
10 MHz INT. REF.	4

10 WAY	PLUG	SKIG
	PIN	PIN
+5v STDBY OSC.	2	2
-1.3v	3	3
CV	4	4,7,10
EXT REF -	6	C
EXT REF +	9	C
SAT REF	10	C

L19 - 6 WAY	PL19	SK19
K19 - 4 WAY	PIN	PIN
	2	2
0MHZ STD C/P	1	1
XT ALRM	5	4
TRIG LEVEL A	6	3
TRIG LEVEL B	4	
+5V		
-5.2V	3	
4 WAY	PL19	SK19
	PIN	
0V	1, 2, 3	
EXT REF.	4	

21 - 20 WAY	PL 21	5K 21
21 - 21 WAY	PIN	PIN
C. POWER	1,2,3	1,2,3,4
SWITCHED DC. POWER	18,19,20	21-24
-5V UR	5,16	6,19
+5V UR	4,17	5,20
RE SENSE	12	15
-11.2V UR	15	18
-11.2V UR	14	17
OV	5,9,10,11	9-14
STDBY	13	16
ATT LOW	6	7

Interconnections

Fig.22